

# Crystal Oscillator

## YSO691ER



### Applications

- Consumer
- Industrial

### Features

- One Time programmable
- PLL Circuit Provides Wide Frequency Range Application (1 ~ 200MHz)
- Low Jitter Design with New Developed IC
- Package Size: 5.0\*3.2, 7.0\*5.0mm

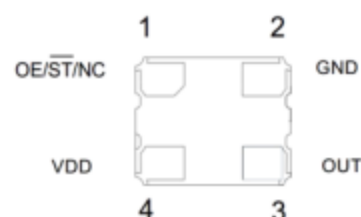
## Specifications (规格参数)

Item / Type	CMOS		
Supply Voltage 电源电压	1.8 V	2.5 V	3.3 V
Frequency Range 额定频率范围	1MHz ~ 125MHz	1MHz ~ 200MHz	1MHz ~ 200MHz
Supply Voltage Variation (VDD)10% 电压范围	1.62V ~ 1.98V	2.25V ~ 2.75V	2.97V ~ 3.63V
Standby Current 待机电流	400uA		
Current Consumption 消耗电流	35mA Max	40mA Max	
Frequency Tolerance 频率偏差	±30ppm, ±50ppm or specify		
Output Load 输出负载	15pF		
Operating Temperature Range 工作温度	-40~+85°C or specify		
Storage Temperature Range 储存温度	-55~+150°C		
Voltage VOH (Min.) / VOL (Max.) 输出电压	VOH = 90%VDD / VOL = 10%VDD		
Duty Cycle 占空比	45~55%		
Rise Time/Fall Time 上升沿/下降沿	5ns Max		
Start-up Time 启动时间	7ms Max		
Phase Jitter [12 kHz ~ 20 MHz] (相位抖动)	1.5ps Max	1.1ps Max	1ps Max
Supply Current 消耗电流	See Below		
Frequency Aging (at 25°C) 老化率	±3 ppm / year Max		

### Pin Dimension (脚位尺寸)

Pin	Symbol	Functionality	
1	OE/ST/NC	Output Enable	H: specified frequency output L: output is low. Specified frequency output stop.
		Standby	H: specified frequency output L: output is low. Device goes to sleep mode. Supply current reduces to 400uA(Standby Current).
		No Connect	Pin 1 = VDD or Pin 1 is Open : Specified frequency output.Pin 1 has no function
2	GND	Power	Electrical ground
3	OUT	Output	Oscillator output
4	VDD	Power	Power supply voltage

Pin Assignments



### Dimensions and Recommended Land Pattern (外观尺寸及推荐焊盘)

Dimensions (Unit: mm)	Recommended Land Pattern (Unit: mm)
<p>5.0*3.2mm</p>	<p>Recommended soldering Pattern</p>
<p>7.0*5.0mm</p>	<p>Recommended soldering Pattern</p>

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## Test Circuit (测试电路)

